



क्रमांक: DoECE/suppli./ 558-C /2026-27

दिनांक: 29-06-2026

Supplementary / End Semester Exam Schedule (July 2026)
Students Under NEP and Non-NEP Scheme

**M. Tech. I – Electronics & Communication Engineering
(1st Semester)**

Exam Date	Time (02:30 PM to 05:30 PM)
13-07-2026	Digital VLSI Design(ECVL101) Digital VLSI Design(EC609)
14-07-2026	Advance Optical Communication Systems(ECCS103) Testing and Verification of VLSI Circuits(ECVL117)
15-07-2026	Advance DSP(ECCS105)
16-07-2026	Information Theory & Coding(ECCS111) Embedded Systems(ECVL105) Embedded Systems(EC613)
17-07-2026	Digital Satellite Communication(ECCS113) Semiconductor IC Technology(ECVL111)
18-07-2026	RF Circuits & Systems(ECCS101) MOS Devices and Technology(ECVL103) MOS Devices and Technology(EC611)

**M. Tech. I – Electronics & Communication Engineering
(2nd Semester)**

Exam Date	Time (02:30 PM to 05:30 PM)
06-07-2026	Image Processing and Computer Vision(ECCS102) Analog VLSI Design (ECVL102)
07-07-2026	5G Wireless Technologies (ECCS128) VLSI System Design (ECVL116)
08-07-2026	Speech Processing and Applications (ECCS130) SOC Design (ECVL118)
09-07-2026	Wireless Communication(ECCS104) Real-Time Systems (ECVL104)
10-07-2026	Deep Learning, Theory and Practice (ECCS172) Fundamentals of Semiconductor Package Manufacturing and Test (ECVL178)

Seating Arrangement: EC022N, Ground floor, Department of Electronics Engineering

Examination Coordinator, DoECE

29/06/2026



शिल्पी गुप्ता
29/6/26
HOD, DoECE

Copy to:

- 1) Dy. Registrar (Acad) 2) Chief Exam Coordinator 3) Student NB DoECE 4) upload@svnit.ac.in 5) All concerned Departments
In case any query, contact Examination Coordinators, DoECE